

**REMARKS**

**Summary of Office Action**

Claims 1-4, 14-17 and 27-28 stand rejected under 35 U.S.C. § 102(e) as allegedly being anticipated by US Patent No. 6,756,951 to *Ishizuka et al.*

Claims 13 and 26 stand rejected under 35 U.S.C. § 103(a) as allegedly being unpatentable by *Ishizuka et al.* in view of U.S. Patent Application No. 2002/0195967 to *Kim et al.*

Claims 5-12 and 18-25 stand objected to as being dependent upon a rejected base claim, but are indicated as being allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

**Summary of Amendment**

The specification has been amended to correct minor typographical errors. No new matter has been added.

Claims 1, 5, 9, 11, 12, 14, 18, 22, 27 and 28 have been amended.

Applicants respectfully traverse the rejection of claims 1-4, 14-17 and 27-28 under 35 U.S.C. § 102(e) as being anticipated by *Ishizuka et al.*

Applicants respectfully traverse the rejection of claims 13 and 26 under 35 U.S.C. § 103(a) as being unpatentable over *Ishizuka et al.* in view of *Kim et al.*

Accordingly, claims 1-28 are currently pending for further consideration.

**Allowable Subject Matter**

Applicants wish to thank the Examiner for indicating allowable subject matter in claims 5-12 and 18-25. Based on the following remarks, Applicants believe claims 1-4, 14-17 and 27-28 are also allowable. Therefore, claims 5-12 and 18-25 have not been rewritten in independent form.

**All Claims Recite Allowable Subject Matter**

Claims 1-4, 14-17 and 27-28 stand rejected under 35 U.S.C. § 102(e) as allegedly being anticipated by *Ishizuka et al.* Claims 13 and 26 stand rejected under 35 U.S.C. § 103(a) as allegedly being unpatentable over *Ishizuka et al.* in view of *Kim et al.* Applicants respectfully traverse the rejections under 35 U.S.C. §§ 102 and 103.

Independent claim 1 recites, in part, a data driver having a plurality of current sink data drive parts in order to supply data to the display panel based on a constant current. Similarly, independent claim 14 recites, in part, that a data driver having a plurality of current source data drive parts in order to supply data to the display panel based on a constant current. The Office asserts that items 200-203 collectively correspond to a data driver. (See Paragraph 3 of the Office Action.) Applicants respectfully disagree. *Ishizuka et al.* discloses independent anode line driving circuits 201, 202 and 203 (i.e., data drivers) as illustrated in Fig. 8. Further, the reference current generating circuit 200 of *Ishizuka et al.* is not a data driver. (See col. 8, lines 37-42.) Thus, each of anode line driving circuits 201, 202 and 203 is an individual data driver

and cannot be viewed as collective components that make up a data driver. Therefore, *Ishizuka et al.* fails to anticipate claims 1 and 14 because each of the anode line driving circuits 201, 202 and 203 do not include a **plurality of current sink data drive parts** (e.g., claim 1) or a **plurality of current source data drive parts** (e.g., claim 14) as claimed.

Claim 1 further recites, in part, that the current sink data drive part comprises a reference current supply/path part for supplying the constant current to the current sink data drive integrated circuit and supplying the same constant current to an adjacent current sink data drive part in a cascade circuit configuration. Similarly, claim 14 further recites, in part, that the current source data drive part comprises a reference current supply/path part for supplying the constant current to the current source data drive integrated circuit and supplying the same constant current to an adjacent current source data drive part in a cascade circuit configuration. By contrast, *Ishizuka et al.* discloses that each of anode line drive control circuits include a drive current control circuit CC that generates a predetermined current  $i$  from input terminal  $I_{in}$  as well as a control current output circuit CO that generates a control current  $i_c$  for output terminal  $I_{out}$  as illustrated by Fig. 11. (See Col. 8, lines 59-63 and col. 9, lines 4-9.) As shown in Fig. 8, an output terminal  $I_{out}$  connects to an adjacent anode line driving circuit rather than an adjacent current sink data drive part (e.g., claim 1) or an adjacent current source data drive part (e.g., claim 14) as claimed. In other words, *Ishizuka et al.* fails to anticipate claims 1 and 14 because the drive current control circuit CC and the control current output circuit CO do not supply the same constant current to an **adjacent current sink data drive part** (e.g., claim 1) or an **adjacent current source data drive part** (e.g., claim 14) as claimed.

Independent claim 27 recites, in part, a current sink data integrated circuit and an adjacent current sink data integrated circuit, which are connected in a cascade circuit configuration within the data driver. Independent claim 28 recites, in part, a current source data integrated circuit and an adjacent current source data integrated circuit, which are connected in a cascade circuit configuration within the data driver. The Office asserts that items 200-203 collectively correspond to a data driver. (See Paragraph 3.) Applicants respectfully disagree. *Ishizuka et al.* discloses independent anode line driving circuits 201, 202 and 203 (i.e., data drivers) as illustrated in Fig. 8. Further, the reference current generating circuit 200 of *Ishizuka et al.* is not a data driver. (See col. 8, lines 37-42.) Thus, each of anode line driving circuits 201, 202 and 203 is an individual data driver and cannot be viewed as collective components that make up a data driver. Therefore, *Ishizuka et al.* does not teach or suggest **a data driver having a sink data integrated circuit and an adjacent current sink data integrated circuit**, which are connected in a cascade circuit configuration **within the data driver** (e.g., claim 27) as claimed. Similarly, *Ishizuka et al.* does not teach or suggest **a current source data integrated circuit and an adjacent current source data integrated circuit**, which are connected in a cascade circuit configuration **within the data driver** (e.g., claim 28) as claimed.

With regard to claims 13 and 26, claim 13 depends from claim 1 and claim 26 depends from claim 14. Because dependent claims incorporate all the features of their base claims, claims 13 and 26 are distinguishable over *Ishizuka et al.* for at least the reasons of their respective base claims 1 and 14 as discussed above. Furthermore, *Kim et al.* does not cure the deficiencies of *Ishizuka et al.* Hence, *Ishizuka et al.* and *Kim et al.*, whether taken singly or in

combination, fail to teach or suggest each feature of claims 13 and 26.

Moreover, *Kim et al.* (US Pat. Pub. 2002/0195967) was commonly owned by the assignee of the present application at the time the present application was filed. Filed concurrently herewith is a verified English translation of the Korean patent application 2002-0051087 filed August 28, 2002 in order to perfect the claimed priority. As *Kim et al.* qualifies as prior art only under §102(e), *Kim et al.* cannot be used in a §103 rejection against the present invention in accordance with 35 U.S.C. §103(c).

For at least the reasons stated above, *Ishizuka et al.* and *Kim et al.*, whether taken singly or in combination, fail to teach or suggest each feature of independent claims 1, 14, 27 and 28. Therefore, the rejections should be withdrawn. Furthermore, Applicants respectfully assert that dependent claims 2-13 and 15-26 are allowable at least because of their dependence on one of claims 1 and 14.

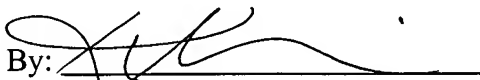
### **CONCLUSION**

In view of the foregoing, reconsideration and timely allowance of the pending claims are respectfully requested. Should the Examiner feel that there are any issues outstanding after consideration of the response, the Examiner is invited to contact the Applicants' undersigned representative to expedite prosecution.

If there are any other fees due in connection with the filing of this response, please charge the fees to our Deposit Account No. 50-0310. If a fee is required for an extension of time under 37 C.F.R. 1.136 not accounted for above, such an extension is requested and the fee should also be charged to our Deposit Account.

Respectfully submitted,

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